



Date: 21st May 2011

Timing: 11:00 am to 1:00pm

Topic 1: CMOS Logic Styles

Topic 2: Sequentials & Memory

**Speaker: Rajaraman Ramanarayanan - Research scientist - Intel
Labs, Intel Technology India Pvt Ltd.**

Brief Bio of the speaker:

Mr. Rajaraman Ramanarayanan received the BE degree from Annamalai University, India, in 2001 and the MS and PhD degrees from the Pennsylvania State University, in 2003 and 2007, respectively.

He joined Intel in January 2007 and is currently a research scientist with Intel Labs, India. His research interests are in the area of energy efficient circuits. He has worked on special purpose and reconfigurable accelerators for arithmetic, DSP and security applications in the past.